



Attorney Docket No.: VN734RI

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Worsley

Serial No.: 09/231,855

Filed: January 14, 1999

For: ASYNCHRONOUS PROCESSOR  
ACCESS TO A SWITCH TABLE  
IN A NETWORK WITH  
ISOCRONOUS CAPABILITY

Examiner: Ton, D.

Group Art Unit: 2732

Assistant Commissioner for Patents  
Washington, D.C. 20231

AMENDMENT

Sir:

In response to the office action mailed September 11, 2000, please reexamine the above-identified application in view of the following amendment and remarks. A petition for extension of time accompanies this amendment and is hereby requested.

IN THE CLAIMS:

19. In a computer controlled system having a processor operating according to a clock and an updatable table operating asynchronously of the clock, the updatable table outputting control data for controlling data transfers in the system, an apparatus, comprising:  
a memory receiving update data from the processor during a first time period at a data rate corresponding to the clock and outputting the update data to the updatable table;  
a control circuit coupled to the memory, wherein the control circuit determines the beginning of a second period of time during which the updatable table is not being used and updates the updatable table during the second period, asynchronously with the clock, using at least some of the update data.